

DOCKET NO. MICHEJDA 4-6

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

John A. Michejda, *et al.*

Serial No.: 09/882,911

Filed: June 15, 2001

For: A SEMICONDUCTOR DEVICE HAVING A GHOST SOURCE
DRAIN REGION AND A METHOD OF MANUFACTURE
THEREFOR

Group No.: 2815

Examiner: Fenty, Jesse A.

Honorable Commissioner of Patents
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service
as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks,
Washington, D.C. 20231, on 6-26-02 (Date)
Elizabeth Schumacher
(Printed or typed name of person signing the certificate)
Elizabeth Schumacher
(Signature of the person signing the certificate)

Sir:

AMENDMENT UNDER 37 C.F.R. § 1.111

In response to the Examiner's Action mailed March 14, 2002, please amend the above-
identified application as follows:

IN THE CLAIMS:

(1) Kindly rewrite Claim 1 as follows:

1. (Amended) A semiconductor device, comprising:
a channel region located in a semiconductor substrate;
a trench located adjacent a side of the channel region;